

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: FUKUDA, KEIKO Art Unit: 4125

Appln. No.: 10/584,395 Examiner: Cole,
Brandon S.

Filed: June 23, 2006 Conf. No.: 9000

For: VOLTAGE GENERATION CIRCUIT AND
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In supplemental response to the Office Action of
February 4, 2008, please amend the above-identified patent
application as indicated below.

Amendments to the claims are reflected in the listing
of claims which begins on page 2 of this paper.

Remarks begin on page 14 of this paper.